

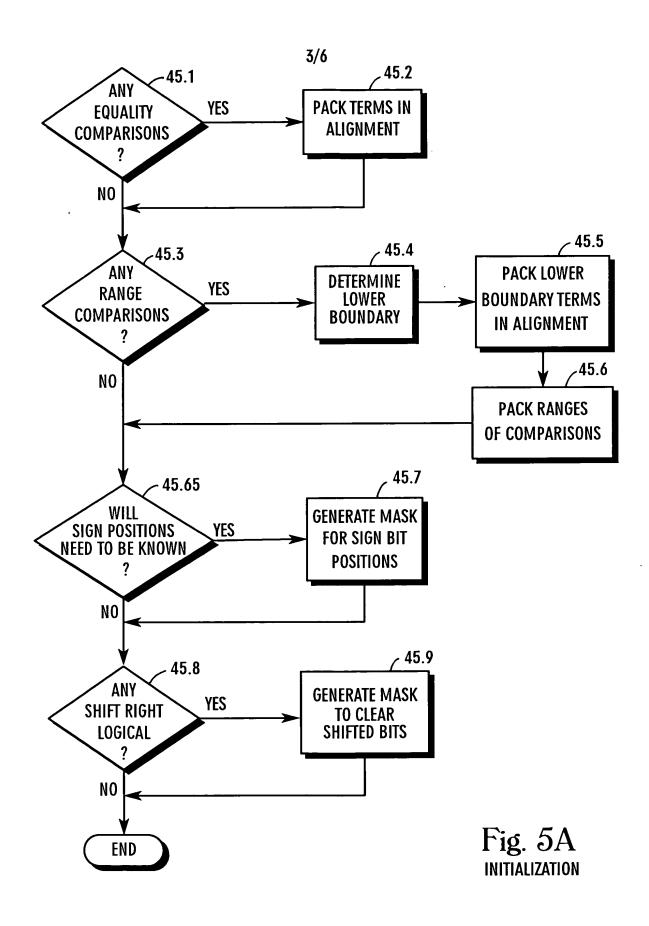
Fig. 6
REGISTER SET UP

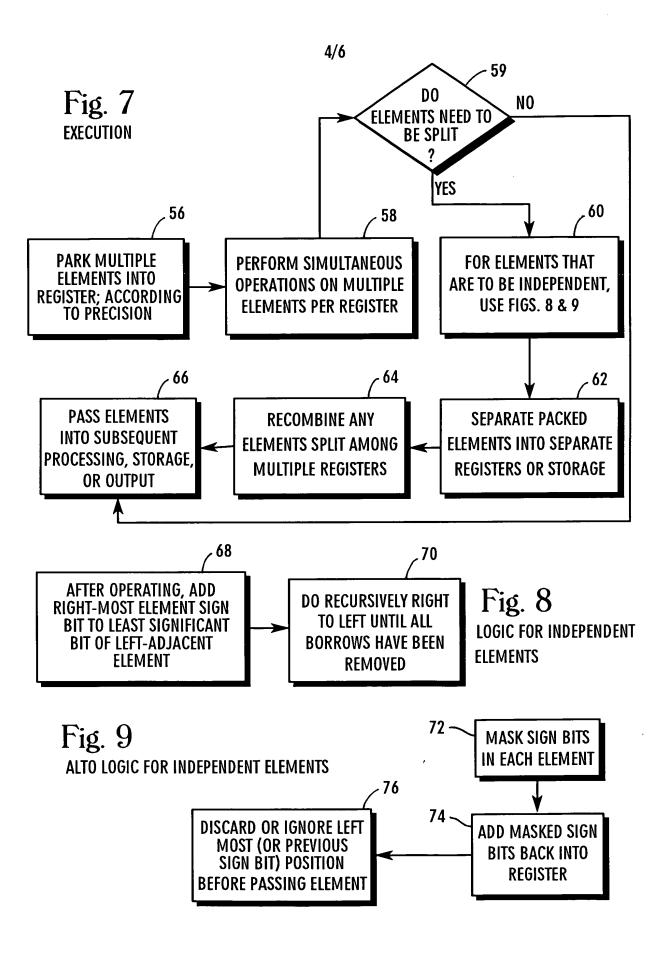
SHIFT NTH VALUE LEFT
BY WIDTH OF 1 ...
NTH ELEMENTS

ADD SHIFTED ELEMENT
TO REGISTER

LOOP UNTIL ALL ELEMENTS
ARE PACKED INTO
REGISTER

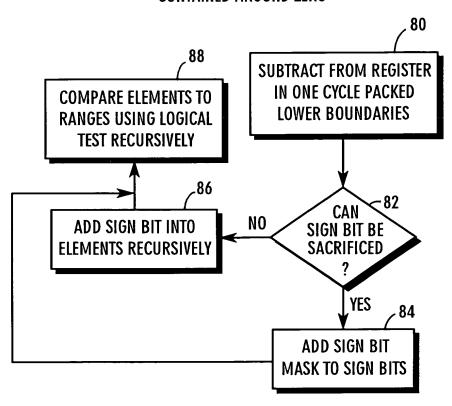
55
END





5/6

Fig. 10
LOGIC FOR DETERMINING WHETHER ELEMENT
CONTAINED AROUND ZERO



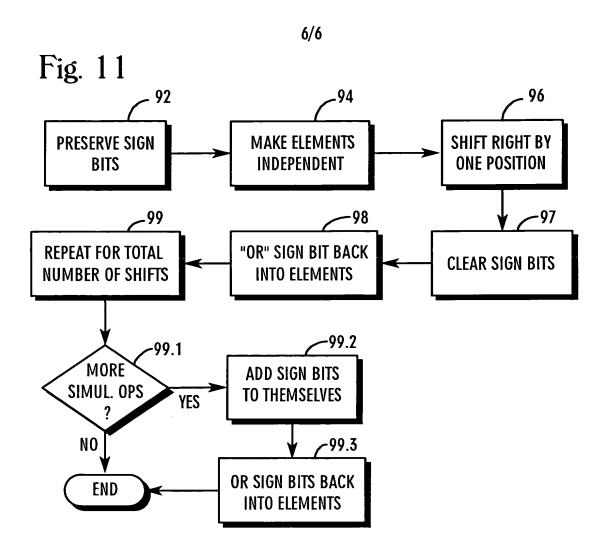


Fig. 12 LOGIC FOR ARITHMETIC COMPARES

